

Form 1449 (Modified) Information Disclosure Statement By Applicant (Use Several Sheets if Necessary)	Atty. Docket No.	Serial No.:
	AGLE0027	09/909,774
	Applicant:	
	Chaiken et al.	
	Filing Date:	Group:
	July 19, 2001	2454 2187

U.S. Patent Documents

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub-class	Filing Date
DAP RE	A	4,695,944	9/22/87	Zandveld et al.	364	200	9/22/86
DAP RE	B	4,723,238	2/2/88	Israel et al.	370	60	3/24/86
DAP RE	C	4,821,265	4/11/89	Albal et al.	370	110.1	4/6/87
DAP RE	D	4,903,258	2/20/90	Kuhlmann et al.	370	58.2	8/19/88
DAP RE	E	4,907,225	3/6/90	Gulick et al.	370	94.1	6/16/89
DAP RE	F	4,939,728	7/3/90	Markkula, Jr. et al.	370	94.3	12/19/88
DAP RE	G	4,949,338	8/14/90	Albal et al.	370	85.6	11/14/88
DAP RE	H	4,955,018	9/4/90	Twitty et al.	370	85.1	11/8/88
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DAP RE	J	4,984,237	1/8/91	Franaszek	370	60	6/29/89
DAP RE	K	4,993,017	2/12/91	Bachinger et al.	370	58.2	2/22/89
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Foreign Patent or Published Foreign Patent Application

Examiner Initial	No.	Document No.	Publication Date	Country or Patent Office	Class	Sub-class	Translation	
							Yes	No
DAP RE	a	0621713	26/10/94	EPO	29	06		X
DAP RE	b	0285330	05/10/8	EPO	11	04		X

Examiner

Ex: DHAIYA A. PATEL

Other Documents

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
DAP RRR	c	*AMD Discloses New Technologies at Microprocessor Forum; October 5, 1999; AMD - News Release #99105; http://www.amd.com/news/prodpr/99105.html ; Printout from INSPEC.
DAP RRR	d	*HyperTransport " Technology Frequently Asked Questions; http://www1.amd.com/products/cpg/showall/1.1395..00.html?type=HyperTransport ; Printout from INSPEC.
DAP RRR	e	*Jayant Matthew; <u>API NetWorks Introduces the First HyperTransport-to-PCI Bridge Chip</u> ; Electronic News; April 2, 2001; Print out from INSPEC.
DAP RRR	f	*Darrell Dunn; <u>API Revving Up Hypertransport Bus - - Looks to raise interest in the technology via chip that links it to PCI</u> ; EBN; April 2, 2001; Print out from INSPEC.
DAP RRR	g	*Jerry Ascierio; <u>Rival post-PCI plans spark talk of bus war</u> ; Electronic Engineering Times; April 2, 2001; Print out from INSPEC.
DAP RRR	h	*Ken Popovich; <u>Intel puts bus plan in gear - Chip maker challenges AMD's HyperTransport Architecture in bid to replace PCI in PC's, servers</u> ; eWeek; March 12, 2001; INSPEC printout.
DAP RRR	i	*Ken Popovich; <u>HyperTransport speeds data transfer (Advanced Micro Devices Inc.)</u> ; eWeek; February 26, 2001; print out from INSPEC.
DAP RRR	j	*Jayant Matthew; <u>AMD Unveils Speedy Chip-to-Chip Link</u> ; Electronic News; February 19, 2001; Print out from INSPEC.
DAP RRR	k	*Linley Gwennap; <u>AMD strikes Lightning</u> ; Electronic Engineering Times; February 19, 2001; print out from INSPEC.
DAP RRR	l	*Jerry Ascierio; <u>Rival post-PCI plans spark talk of bus war</u> ; Electronic Engineering Times; 2001, n 1160, April 2, 2001; print out from INSPEC.
DAP RRR	m	*Dave Polifko ; <u>The Optical Internet: Taking Up Where SONET Leaves Off</u> ; Telecommunications; June 2000; print out from INSPEC.
DAP RRR	n	*Chu-Sun Yen, Richard C. Walker, Patrick T. Petruno; Cheryl Stout; Benny W.H. Lai; and William J. McFarland; <u>G-Link: A chipset for gigabit-rate data communication</u> ; Hewlett-Packard Journal; October 1992; print out from INSPEC.
DAP RRR	o	*Mark Miller; <u>Which route for routers?</u> ; <u>Buyer's Guide, As vendors relocate the function of routers, look for those devices to take on many new faces</u> ; Network World; august 28, 1995; INSPEC print out.

Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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